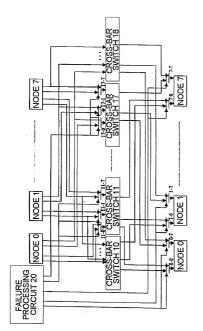
FIG . 1



I/O ADAPTER

FIG. 2

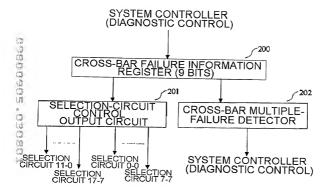
CPU CPU CPU CPU CPU

CONTROLLER MEMORY

CONTROLLER MEMORY

**CROSS-BAR SWITCH** 

FIG . 3



## 

FIG . 4

	BYTE 7	CROSS-BAR SWITCH 18	CROSS-BAR SWITCH 17	CROSS-BAR SWITCH 17							
	BYTE 6	CROSS-BAR SWITCH 17	CROSS-BAR SWITCH 16	CROSS-BAR SWITCH 16	GROSS-BAR SWITCH 16						
	BYTE 5	CROSS-BAR SWITCH 16	CROSS-BAR SWITCH 15	CROSS-BAR SWITCH 15	CROSS-BAR SWITCH 15	CROSS-BAR SWITCH 15					
	BYTE 4	GROSS-BAR SWITCH 15	CROSS-BAR SWITCH 14								
	BYTE 3	CROSS-BAR SWITCH 14	GROSS-BAR SWITCH 14	CROSS-BAR SWITCH 14	CROSS-BAR SWITCH 14	CROSS-BAR SWITCH 13					
	BYTE 2	CROSS-BAR SWITCH 13	CROSS-BAR SWITCH 13	CROSS-BAR SWITCH 13	CROSS-BAR SWITCH 12						
	BYTE 1	CROSS-BAR SWITCH 12	CROSS-BAR SWITCH 12	CROSS-BAR SWITCH 11							
	BYTE 0	CROSS-BAR SWITCH 11	CROSS-BAR SWITCH 10								
EALII TV	CROSS-BAR SWITCH	CROSS-BAR SWITCH 10	CROSS-BAR SWITCH 11	CROSS-BAR SWITCH 12	CROSS-BAR SWITCH 13	CROSS-BAR SWTCH 14	CROSS-BAR SWITCH 15	CROSS-BAR SWITCH 16	CROSS-BAR SWITCH 17	CROSS-BAR SWITCH 18	NONE FAULTY